



10/772,606

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Joseph E. Geusic et al. Examiner: Julio Maldonado
Serial No.: 10/772,606 Group Art Unit: 2823
Filed: February 5, 2004 Docket: 303.390US4
Title: INTEGRATED CIRCUITS USING OPTICAL FIBER INTERCONNECTS
FORMED THROUGH A SEMICONDUCTOR WAFER

RESPONSE TO RESTRICTION REQUIREMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the Restriction Requirement mailed October 26, 2004, Applicant elects, without traverse, Species 1 having claims directed to a semiconductor device including one substrate, with claims 1-15 and 34-40 readable thereon.

The Examiner is invited to contact Applicant's Representatives at the below-listed telephone number if there are any questions regarding this Response or if prosecution of this application may be assisted thereby.

Respectfully submitted,

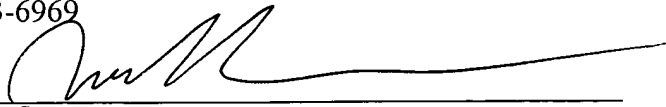
JOSEPH E. GEUSIC ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 373-6969

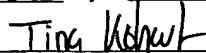
Date 23 November 2004

By


Viet V Tong
Reg. No. 45,416

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 23 day of November, 2004.

Name



Signature

